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November 3, 2003

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran
Assignee: Lattice Semiconductor Corporation
Title: Scalable Serializer-Deserializer Architecture And Programmable Interface
Serial No.: 10/619,645 Filed: 07/14/2003
Examiner: Unknown Group Art Unit: 2171
Docket No.: M-15135 US

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate);
- (3) Response to Notice to File Corrected Application Papers;
- (4) A copy of the Notice to File Corrected Application Papers;
- (5) Submission of formal drawings with sixteen (16) sheets of formal drawings including Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, and 17; and
- (6) Change of Correspondence Address.



No additional fee is required.



The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After</u> <u>Amendment</u>		Highest No. Previously <u>Paid</u> <u>For</u>		Present <u>Extra</u>		<u>Rate</u>		<u>Additional Fee</u>
Total Claims	20	Minus	20	=	0	x	\$18.00	\$	0
Independent Claims	3	Minus	3	=	0	x	\$84.00	\$	0
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application								\$
<input type="checkbox"/>	Fee for Request for Extension of Time								\$

Total additional fee for this Amendment:

\$



Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.



Please charge our Deposit Account No. 50-2257 in the amount of

\$



Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257

Total:

\$

0

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 3, 2003.

Eric Hoover

November 3, 2003
Date of Signature

Respectfully submitted,

Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran
Assignee: Lattice Semiconductor Corporation
Title: Scalable Serializer-Deserializer Architecture And Programmable Interface
Serial No.: 10/619,645 Filing Date: 07/14/2003
Examiner: Unassigned Group Art Unit: 2171
Docket No.: M-15135 US

Irvine, California
November 3, 2003

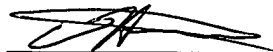
Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit sixteen (16) sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, and 17, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on November 3, 2003.


Eric Hoover

November 3, 2003

Respectfully submitted,



Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940

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